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# Intellectual Property Law

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Message:

Application No.

09/751,762

Confirmation No. 2183

Applicant

Sailesh KOTTAPALLI et al.

Filed

December 29, 2000

Title

METHOD FOR CONVERTING PIPELINE STALLS CAUSED BY

INSTRUCTIONS WITH LONG LATENCY MEMORY ACCESSES TO PIPELINE FLUSHES IN A MULTITHREADED PROCESSOR (as

amended)

TC/A.U.

2183

Examiner

Daniel H. PAN

PAPER(s) ENTITLED:

Amendment

9 pages

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#### MAR 2 1 2006

Patent

Attorney Docket No.: Intel 2207/10121 Serial No.: 09/751,762 Assignee: Intel Corporation

#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**APPLICANT** 

Sailesh KOTTAPALLI et al.

SERIAL NO.

09/751,762

**FILED** 

December 29, 2000

FOR

METHOD FOR CONVERTING PIPELINE STALLS

CAUSED BY INSTRUCTIONS WITH LONG LATENCY MEMORY ACCESSES TO PIPELINE FLUSHES IN A

MULTITHREADED PROCESSOR [as amended]

GROUP ART UNIT

2183

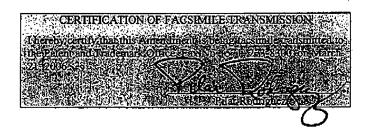
**EXAMINER** 

Daniel H. PAN

M/S: AMENDMENTS Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450



### **AMENDMENT**

#### Dear Sir:

The following amendments and remarks below are respectfully submitted in response to the Office Action dated December 21, 2005

Amendments to the Specification begin on page 2 of this response.

Listing of the Claims begin on page 3 of this response.

Remarks/ Arguments begin on page 7 of this response.

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